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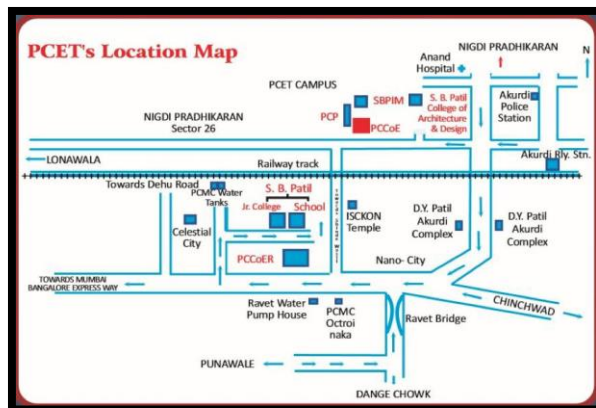
## CONVENOR

Dr. N. B. Chopade, H. O. D, E&TC, PCCOE

## ORGANIZING COMMITTEE

Dr. M.T. Kolte  
Prof. V. K. Harpale  
Prof. Varsha Bendre  
Prof. Swati Jagtap  
Prof. Archana Bhamare  
Mr. Pradip Sandbhor  
Mrs. M. P. Chinchkar

## LOCATION MAP



## ABOUT P.C.E.T.

Pimpri Chinchwad Education Trust (PCET) was established by Late Shri. S. B. Patil in the year 1990 with a vision to provide value added educational platform to society in multiple dimensions. PCET is a well-established and time tested brand in Education since 1990.

## ABOUT P.C.C.O.E.

Pimpri Chinchwad College of Engineering (PCCOE), Pune was established in 1999-2000. The institute is run by Pimpri Chinchwad Education Trust. PCCOE has more than three thousand students on campus. PCCOE has all state-of-art infrastructural facilities in the developing industrial belt of PCMC and is a 'Clean and Green Campus'. PCCOE offers AICTE approved courses like UG – Mechanical Engineering, E&TC Engineering, Computer Engineering, Civil Engineering, and Information Technology and PG courses in Mechanical Engineering (specialization in Design and Heat Power Engineering), E&TC (specialization in Embedded Systems and VLSI), Computer Engineering, Information Technology, MBA and MCA. PCCOE is approved By AICTE, New Delhi and DTE, Mumbai. Also PCCOE has received permanent affiliation from Savitribai Phule Pune University. It is also an ISO 9001:2015 Certified Institute.

## ABOUT DEPARTMENT

The Electronics and Telecommunication Engineering Department was established in year 2001. The department is enriched with many laboratories with sophisticated instruments. Industries offer sponsorship to projects for undergraduate and postgraduate students. Department is accredited by National Board of Accreditation (NBA, AICTE, New Delhi) and has recognized research center under SPPU

PCET's

**PIMPRI CHINCHWAD  
COLLEGE OF ENGINEERING, PUNE**  
(An ISO 9001:2015 Certified Institute)



**Department of Electronics and  
Telecommunication Engineering**

*in association with*

**IETE-PCCOE Student Forum**

*organizes*

**One Week Short Term Training  
Programme (STTP)**

*on*

**“Electronics System Design”**

**(14<sup>th</sup>– 18<sup>th</sup> July, 2017)**

## COORDINATORS

**Dr. M. T. Kolte**

**Prof. Varsha Bendre Prof. V. K. Harpale**

## CONTACT DETAILS

**Electronics & Telecommunication Engg. Dept.  
Pimpri Chinchwad College of Engineering,  
Sector no.26, Near Akurdi Railway Station,  
Nigdi, Pune – 411044  
Maharashtra**

**Mr. Nilesh Komatwar 7776090443**

## ABOUT THE WORKSHOP

The aim of this workshop is to provide in depth understanding of design of electronic circuits and systems with a focus on design of switched mode power supply, Data Acquisition System and design of communication block. This programme will provide knowledge, methods, and tools for modeling and design of components, circuits, and systems needed in development of above mentioned systems. A theoretical aspect will be first covered which includes wide field within electronics system designs constraints i.e. power supply, interfacing of sensors to controllers and development of Data Acquisition system with relevant applications, design of audio/ power amplifiers. This is further extended to understanding of DBMS and SQL which serves as a backbone for development of databases in various simulation softwares and few insights on PCB artwork design. Thus, overall electronic system design flow will be addressed to give a good understanding of the different tactics in design, their scope and limitations.

## OBJECTIVES

1. To apply the fundamental concepts and working principles of electronics devices to design electronics systems.
2. To inculcate circuit designing skills ability to use modern design tools.
3. To design an electronic system/sub-system validate its performance through simulation
4. To create, manage the database and query handling using suitable tools.

## TECHNICAL FACILITIES

- E-Yantra lab set up sponsored by MHRD, India and PCB design and development set up
- Advanced software tools like Mentor graphics, MATLAB and Simulink, MULTISIM and ULTIBOARD etc.
- Advanced Equipments OTDR, Logic Analyzer and High end boards Spartan6, Vertex7 and Zed board

## RESOURCE PERSONS

Mr. Sanjay Joglekar, (Kanaad Electromotion Solutions Pvt Ltd, Sangli)  
Dr. Pravin Futane, Professor, Department of Computer Engineering, PCCOE, Pune  
Dr. Nilesh Uke, Professor, Department of Information Technology, PCCOE, Pune

## REGISTRATION DETAILS

Category	Registration Fees
Industry Delegates	Rs.3000/-
U. G. / P. G. Faculty	Rs.1800/-
U. G. / P. G. Students	Rs.500/-

Fees include certificate, workshop kit, lunch, breakfast & tea during respective days.

Payment can be made by a **Cash /Demand Draft/Cheque in favour of "The Principal, Pimpri Chinchwad College of Engineering, Pune", payable at Pune.**

## Registration Form

### One Week STTP

on

### **"Electronics System Design"**

**(14<sup>th</sup>-18<sup>th</sup>, July, 2017)**

Name.....

Designation.....

Department.....

Organization/Institute.....

Address.....

.....Pin.....

Phone (Off.).....

(Res.).....

Mobile No. ....

Email.....

Mode of fee payment (Cash/DD).....

DD. No. ....Date.....

Amount.....Bank.....

Signature of Applicant

Signature & Seal

Head of the Institution

### **Contacts for Registration:**

Prof. Swati Jagtap (9226094918)

Prof. Archana Bhamare (9527137558)