

FACULTY PROFILE

1. Name : Mr. Ajay Sharad Gadhe
2. Date of Birth : 24/11/1988
3. Qualification : M.Tech (VLSI Technology)
4. Designation : Assistant Professor
5. Mobile No : 07709775815
6. Email-ID : ajay.gadhe@gmail.com
7. Experience : 2 Years
8. Area of Interest : VLSI Design, Embedded Systems, Low power SRAM design, RTL coding of digital circuits in VHDL/Verilog, CMOS level designing, Object oriented programming using C++, JAVA.
9. Subjects Taught (UG) : System Programming and Operating Systems (SEM I),
Antenna and Wave Propagation Theory (SEM II)

10. Association with Professional bodies : -

11. Publications:

Journal Papers:

1. Ajay Gadhe, Ujwal Shirode “**Read stability and Write ability analysis of different SRAM cell structures,**” International Journal of Engineering Research and Applications Vol. 3, Issue 1, January -February 2013, pp.1073-1078.
2. Ajay Gadhe, Ujwal Shirode “**Read stability and read failure analysis of low voltage Schmitt Trigger based SRAM bit cell,**” International Journal of Engineering Research and Applications, Vol. 3, Issue 1, January-February 2013, pp.876-879.

12. Workshops/STTP/FDPs : -

13. Reviewer for Journal or Conferences : -

14. Research Project Grants : -

15.Research projects guided or undertaken/ Sponsored Projects/ Interaction with industry or outside world : -

16. Invited Talks : - -

17. Evaluator for different activities : -

18. Other(Major Portfolios handled at College/ Department / University level)

1. Unit Test Co-Ordinator of E&TC Department for year 2015-16(Sem-I).

2. Class Teacher of Third Year, E&TC , Div A for Year 2015-16 .(Sem-II)